



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jong Chan

Examiner: Michael C. Maskulinski

Serial No.: 09/846,868

Group Art Unit: 2184

Filed: May 1, 2001

Docket No.: 10980422-3

Title: MEMORY CONTROLLER SUPPORTING REDUNDANT
SYNCHRONOUS MEMORIES

Calh
3/24/04

AMENDMENT AND RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This Amendment and Response is in reply to the Non-Final Office Action mailed December 15, 2003. Please amend the above-identified patent application as follows:

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Technology Center 2100